## SIEMENS

## Fail-safe CPUs

## Overview

The fail-safe SIMATIC S7-1200 Controllers are based on the S7-1200 standard CPUs and offer additional safety-related functions.
They can be used for safety-related tasks according to IEC 61508 up to SIL 3 and ISO 13849-1 up to PL e.
Safety-related programs are created in the TIA Portal. The STEP 7 Safety engineering tool offers commands, operations and blocks for safety-related programs in the LAD and FBD languages. To this end, there is a library with pre-configured blocks for safety-related functions certified by the German Technical
Inspectorate (TÜV).

- Standard controller with integrated safety functions:
- Standardized and convenient diagnostic functions for standard and safety
- Uniform symbols, data consistency, ...
- Modular system with scalable range of CPUs and expandable I/O quantity structure:
- One engineering for standard and fail-safe automation
- Use of the standard I/O modules together with the fail-safe I/O modules in the central system
- Integrated standard PROFINET functionalities for PROFINET controllers and PROFINET iDevice services
- Connection of distributed standard I/O via fieldbus such as PROFINET or PROFIBUS
- F-library certified by the German Technical Inspectorate (TÜV) for all common safety functions
- Free programming of the safety logic using FBD and LAD
- Standard-compliant printout of the F program
- One integrated engineering for both standard and safety from S7-1200 to S7-300/400/1500 and WinAC RTX F:
- STEP 7 Safety Basic for easy engineering of the CPU 1200 FC
- STEP 7 Safety Advanced for the entire fail-safe SIMATIC S7 portfolio
- Integrated system diagnostics of the CPUs, for standard and safety:
- Consistent plain text display of system diagnostics information in the TIA Portal, HMI and web server
- Messages are updated even if the CPU is in STOP state
- System diagnostics integrated in the CPU firmware. Configuration by user not required
- The diagnostics is automatically updated on configuration changes.
- 2 fail-safe compact controllers with graded performances in the versions $D C / D C / D C$ and $D C / D C / r e l a y$

| Characteristics | CPU 1212 FC | CPU 1214 FC | CPU 1215 FC |
| :---: | :---: | :---: | :---: |
| Variants | DC/DC/DC, DC/DC/relay | DC/DC/DC, DC/DC/relay | DC/DC/DC, DC/DC/relay |
| Work memory, integrated | 100 KB | 125 KB | 150 KB |
| Load memory, integrated | 2 MB | 4 MB | 4 MB |
| Memory card | SIMATIC Memory Card (optional) | SIMATIC Memory Card (optional) | SIMATIC Memory Card (optional) |
| Standard digital inputs/outputs, integrated | 8/6 | 14/10 | 14/10 |
| Standard analog inputs, integrated | 2 | 2 | 2 |
| Standard analog outputs, integrated | - | - | 2 |
| Process image | 1024 bytes for inputs, 1024 bytes for outputs | 1024 bytes for inputs, 1024 bytes for outputs | 1024 bytes for inputs, 1024 bytes for outputs |
| Expansion by signal board | Max. 1 | Max. 1 | Max. 1 |
| Expansion by signal modules | Max. 2 | Max. 8 | Max. 8 |
| Expansion by communications modules | Max. 3 | Max. 3 | Max. 3 |

## Application

SIMATIC S7-1200 is the ideal controller for local and distributed automation solutions with safety requirements in the central configuration
Via the engineering, the fail-safe SIMATIC S7-1200 Controller makes preassembled, tested and TÜV/German Technical Inspectorate-certified blocks available for implementing all common safety functions, such as EMERGENCY STOP or protective door monitoring, with or without interlocking.

- CPU 1212FC:

The ideal compact solution for standard and fail-safe applications

- CPU 1214 FC:

The compact CPU for standard and fail-safe applications

- CPU 1215 FC:

The compact CPU with two PROFINET ports for standard and fail-safe applications

## Design

## Mechanical features

- Horizontal or vertical mounting on DIN rail or direct mounting in the cabinet using integral drill holes (not horizontal).
- Terminal block for independent wiring for all CPUs and associated components.


## Technical specifications

| Article number | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { 0XB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{aligned} & \text { CPU 1212FC } \\ & \text {,DC/DC/DC, } \\ & \text { 8DI/6DO/2AI } \end{aligned}$ | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | $\begin{aligned} & \text { CPU } 1214 \text { FC, } \\ & \text { DC/DC/DC, } \\ & \text { 14DI/10DO/2AI } \end{aligned}$ | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, DC/DC/DC, 14DI/10DO/2AI/2AO | CPU 1215 FC, <br> DC/DC/RLY,14DI/10DO/2AI/2AO |
| General information |  |  |  |  |  |  |
| Product type designation | CPU 1212FC <br> DC/DC/DC | CPU 1212FC DC/DC/relay | CPU 1214FC DC/DC/DC | CPU 1214FC DC/DC/Relay | CPU 1215FC DC/DC/DC | CPU 1215FC DC/DC/relay |
| Firmware version | V4.5 | V4.5 | V4.5 | V4.5 | V4.5 | V4.5 |
| Engineering with |  |  |  |  |  |  |
| - Programming package | STEP 7 V17 or higher | STEP 7 V17 or higher | STEP 7 V17 or higher | STEP 7 V17 or higher | STEP 7 V17 or higher | STEP 7 V 17 or higher |
| Supply voltage |  |  |  |  |  |  |
| Rated value (DC) |  |  |  |  |  |  |
| - 24 V DC | Yes | Yes | Yes | Yes | Yes | Yes |
| permissible range, lower limit (DC) | 20.4 V | 20.4 V | 20.4 V | 20.4 V | 20.4 V | 20.4 V |
| permissible range, upper limit (DC) | 28.8 V | 28.8 V | 28.8 V | 28.8 V | 28.8 V | 28.8 V |
| Reverse polarity protection | Yes |  | Yes |  | Yes | Yes |
| Load voltage L+ |  |  |  |  |  |  |
| - Rated value (DC) | 24 V | 24 V | 24 V | 24 V | 24 V | 24 V |
| - permissible range, lower limit (DC) | 20.4 V | 20.4 V | 20.4 V | 20.4 V | 20.4 V | 20.4 V |
| - permissible range, upper limit (DC) | 28.8 V | 28.8 V | 28.8 V | 28.8 V | 28.8 V | 28.8 V |
| Input current |  |  |  |  |  |  |
| Current consumption (rated value) | $\begin{aligned} & 400 \mathrm{~mA} \text {; CPU } \\ & \text { only } \end{aligned}$ | $\begin{aligned} & 400 \mathrm{~mA} ; \mathrm{CPU} \\ & \text { only } \end{aligned}$ | $\begin{aligned} & 500 \mathrm{~mA} \text {; CPU } \\ & \text { only } \end{aligned}$ | $\begin{aligned} & 500 \mathrm{~mA} ; \mathrm{CPU} \\ & \text { only } \end{aligned}$ | 500 mA ; CPU only | 500 mA ; CPU only |
| Current consumption, max. | 1200 mA; CPU with all expansion modules | 1200 mA; CPU with all expansion modules | $\begin{aligned} & 1500 \mathrm{~mA} \text {; CPU } \\ & \text { with all } \\ & \text { expansion } \\ & \text { modules } \end{aligned}$ | $\begin{aligned} & 1500 \mathrm{~mA} ; \mathrm{CPU} \\ & \text { with all } \\ & \text { expansion } \\ & \text { modules } \end{aligned}$ | 1500 mA ; CPU with all expansion modules | 1500 mA ; CPU with all expansion modules |
| Inrush current, max. | $\begin{aligned} & 12 \mathrm{~A} \text {; at } 28.8 \mathrm{~V} \\ & \mathrm{DC} \end{aligned}$ | 12 A ; at 28.8 V | 12 A ; at 28.8 V | 12 A ; at 28.8 V | 12 A ; at 28.8 V DC | 12 A ; at 28.8 V DC |
| $1^{2} \mathrm{t}$ | $0.5 \mathrm{~A}^{2} \mathrm{~s}$ | $0.8 \mathrm{~A}^{2} \mathrm{~s}$ | $0.5 \mathrm{~A}^{2} \mathrm{~s}$ | $0.8 \mathrm{~A}^{2}$. ${ }^{\text {c }}$ | $0.5 \mathrm{~A}^{2} \mathrm{~s}$ | $0.5 \mathrm{~A}^{2} \mathrm{~s}$ |
| Output current |  |  |  |  |  |  |
| for backplane bus ( 5 V DC), max. | 1000 mA ; Max. 5 V DC for SM and CM | 1000 mA ; Max. 5 V DC for SM and CM | 1600 mA ; Max. 5 V DC for SM and CM | 1600 mA ; Max. 5 V DC for SM and CM | 1600 mA; Max. 5 V DC for SM and CM | $\begin{aligned} & 1600 \mathrm{~mA} \text {; Max. } 5 \mathrm{~V} \text { DC for SM } \\ & \text { and CM } \end{aligned}$ |
| Encoder supply |  |  |  |  |  |  |
| 24 V encoder supply |  |  |  |  |  |  |
| - 24 V | $\begin{aligned} & \mathrm{L}+\text { minus } 4 \mathrm{~V} \\ & \mathrm{DC} \text { min. } \end{aligned}$ | $\begin{aligned} & \mathrm{L}+\text { minus } 4 \mathrm{~V} \\ & \mathrm{DC} \text { min. } \\ & \hline \end{aligned}$ | $\begin{aligned} & \mathrm{L}+\text { minus } 4 \mathrm{~V} \\ & \mathrm{DC} \text { min. } \end{aligned}$ | $\begin{aligned} & \mathrm{L}+\text { minus } 4 \mathrm{~V} \\ & \mathrm{DC} \text { min. } \end{aligned}$ | L+ minus 4 V DC min . | L+ minus 4 V DC min. |
| Power loss |  |  |  |  |  |  |
| Power loss, typ. | 9 W | 9 W | 12 W | 12 W | 12 W | 12 W |
| Memory |  |  |  |  |  |  |
| Work memory |  |  |  |  |  |  |
| - integrated | 100 kbyte | 100 kbyte | 125 kbyte | 125 kbyte | 150 kbyte | 150 kbyte |
| - expandable | No | No | No | No | No | No |
| Load memory |  |  |  |  |  |  |
| - integrated | 2 Mbyte | 2 Mbyte | 4 Mbyte | 4 Mbyte | 4 Mbyte | 4 Mbyte |
| - Plug-in (SIMATIC <br> Memory Card), max. | with SIMATIC memory card | with SIMATIC memory card | with SIMATIC memory card | with SIMATIC memory card | with SIMATIC memory card | with SIMATIC memory card |
| Backup |  |  |  |  |  |  |
| - present | Yes | Yes | Yes | Yes | Yes | Yes |
| - maintenance-free | Yes | Yes | Yes | Yes | Yes | Yes |
| - without battery | Yes | Yes | Yes | Yes | Yes | Yes |
| CPU processing times |  |  |  |  |  |  |
| for bit operations, typ. | $0.08 \mu \mathrm{~s} ; /$ instruction | $0.08 \mu \mathrm{~s} ; /$ instruction | $0.08 \mu \mathrm{~s} ; /$ instruction | $\begin{aligned} & 0.08 \mu \mathrm{~s} ; / \\ & \text { instruction } \end{aligned}$ | $0.08 \mu \mathrm{~s}$ / instruction | $0.08 \mu \mathrm{~s} ; /$ instruction |
| for word operations, typ. | $1.7 \mu \mathrm{~s}$; / instruction | $1.7 \mu \mathrm{~s}$; / instruction | $1.7 \mu \mathrm{~s}$; / instruction | $1.7 \mu \mathrm{~s}$; / instruction | 1.7 ss; / instruction | 1.7 ss; / instruction |
| for floating point arithmetic, typ. | $2.3 \mu \mathrm{~s}$; / instruction | $2.3 \mu \mathrm{~s}$; / instruction | $2.3 \mu \mathrm{~s}$; / instruction | $2.3 \mu \mathrm{~s}$; / instruction | $2.3 \mu \mathrm{~s}$; / instruction | 2.3 ss; / instruction |
| CPU-blocks |  |  |  |  |  |  |
| Number of blocks (total) | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used | DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used |
| OB |  |  |  |  |  |  |
| - Number, max. | Limited only by RAM for code | Limited only by RAM for code | Limited only by RAM for code | Limited only by RAM for code | Limited only by RAM for code | Limited only by RAM for code |
| Data areas and their retentivity |  |  |  |  |  |  |
| Retentive data area (incl. timers, counters, flags), max. | 14 kbyte | 14 kbyte | 14 kbyte | 14 kbyte | 14 kbyte | 14 kbyte |
| Flag |  |  |  |  |  |  |
| - Size, max. | 4 kbyte; Size of bit memory address area | 4 kbyte; Size of bit memory address area | 8 kbyte; Size of bit memory address area | 8 kbyte; Size of bit memory address area | 8 kbyte; Size of bit memory address area | 8 kbyte; Size of bit memory address area |
| Local data |  |  |  |  |  |  |
| - per priority class, max. | 16 kbyte; Priority class 1 (program | 16 kbyte; Priority class 1 (program | 16 kbyte; Priority class 1 (program | 16 kbyte; Priority class 1 (program | 16 kbyte; Priority class 1 (program | 16 kbyte; Priority class 1 (program cycle): 16 KB , priority |


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| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, 14DI/10DO/2AI | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2A | CPU 1215 FC, DC/DC/DC, 14DI/10DO/2AI/2AO | CPU 1215 FC, <br> DC/DC/RLY,14DI/10DO/2AI/2AO |
|  | cycle): 16 KB , priority class 2 to 26: 6 KB | cycle): 16 KB , priority class 2 to 26: 6 KB | cycle): 16 KB , priority class 2 to 26: 6 KB | cycle): 16 KB , priority class 2 to 26: 6 KB | cycle): 16 KB, priority class 2 to 26: 6 KB | class 2 to 26: 6 KB |
| Address area |  |  |  |  |  |  |
| Process image |  |  |  |  |  |  |
| - Inputs, adjustable | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte |
| - Outputs, adjustable | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte | 1 kbyte |
| Hardware configuration |  |  |  |  |  |  |
| Number of modules per system, max. | 3 comm. modules, 1 signal board, 2 signal modules | 3 comm. modules, 1 signal board, 2 signal modules | 3 comm. modules, 1 signal board, 8 signal modules | 3 comm. modules, 1 signal board, 8 signal modules | 3 comm. modules, 1 signal board, 8 signal modules | 3 comm. modules, 1 signal board, 8 signal modules |
| Time of day |  |  |  |  |  |  |
| Clock |  |  |  |  |  |  |
| - Hardware clock (realtime) | Yes | Yes | Yes | Yes | Yes | Yes |
| - Backup time | 480 h ; Typical | 480 h ; Typical | 480 h ; Typical | 480 h ; Typical | 480 h ; Typical | 480 h ; Typical |
| - Deviation per day, max. | $60 \mathrm{~s} / \mathrm{month}$ at 25 ${ }^{\circ} \mathrm{C}$ | $\pm 60$ s/month at $25^{\circ} \mathrm{C}$ | $\pm 60 \mathrm{~s} /$ month at $25^{\circ} \mathrm{C}$ | $\pm 60 \mathrm{~s} / \mathrm{month}$ at $25^{\circ} \mathrm{C}$ | $\pm 60 \mathrm{~s} / \mathrm{month} \text { at } 25$ ${ }^{\circ} \mathrm{C}$ | $\pm 60 \mathrm{~s} / \mathrm{month}$ at $25^{\circ} \mathrm{C}$ |
| Digital inputs |  |  |  |  |  |  |
| Number of digital inputs | 8; Integrated | 8; Integrated | 14; Integrated | 14; Integrated | 14; Integrated | 14; Integrated |
| - of which inputs usable for technological functions | 4; HSC (High Speed Counting) | 4; HSC (High Speed Counting) | 6; HSC (High Speed Counting) | 6; HSC (High Speed Counting) | 6; HSC (High Speed Counting) | 6; HSC (High Speed Counting) |
| Source/sink input | Yes | Yes | Yes | Yes | Yes | Yes |
| Number of simultaneously controllable inputs all mounting positions |  |  |  |  |  |  |
| - up to $40^{\circ} \mathrm{C}$, max. | 8 | 8 | 14 | 14 | 14 | 14 |
| Input voltage |  |  |  |  |  |  |
| - Rated value (DC) | 24 V | 24 V | 24 V | 24 V | 24 V | 24 V |
| - for signal "0" | 5 VDC at 1 mA | 5 VDC at 1 mA | 5 VDC at 1 mA | 5 VDC at 1 mA | 5 VDC at 1 mA | 5 VDC at 1 mA |
| - for signal "1" | $\begin{aligned} & 15 \mathrm{~V} D C \text { at } 2.5 \\ & \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & 15 \mathrm{~V} \text { DC at } 2.5 \\ & \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & 15 \mathrm{~V} \text { DC at } 2.5 \\ & \mathrm{~mA} \end{aligned}$ | $\begin{aligned} & 15 \mathrm{~V} D C \text { at } 2.5 \\ & \mathrm{~mA} \end{aligned}$ | 15 V DC at 2.5 mA | 15 VDC at 2.5 mA |
| Input delay (for rated value of input voltage) |  |  |  |  |  |  |
| for standard inputs |  |  |  |  |  |  |
| - parameterizable | $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}$, $0.8 \mathrm{~ms}, 1.6 \mathrm{~ms}$, $3.2 \mathrm{~ms}, 6.4 \mathrm{~ms}$ and 12.8 ms , selectable in groups of four | $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}$, $0.8 \mathrm{~ms}, 1.6 \mathrm{~ms}$, $3.2 \mathrm{~ms}, 6.4 \mathrm{~ms}$ and 12.8 ms , selectable in groups of four | $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}$, $0.8 \mathrm{~ms}, 1.6 \mathrm{~ms}$, $3.2 \mathrm{~ms}, 6.4 \mathrm{~ms}$ and 12.8 ms , selectable in groups of four | $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}$, $0.8 \mathrm{~ms}, 1.6 \mathrm{~ms}$, $3.2 \mathrm{~ms}, 6.4 \mathrm{~ms}$ and 12.8 ms , selectable in groups of four | $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}, 0.8$ $\mathrm{ms}, 1.6 \mathrm{~ms}, 3.2 \mathrm{~ms}$, 6.4 ms and 12.8 ms , selectable in groups of four | Yes; $0.2 \mathrm{~ms}, 0.4 \mathrm{~ms}, 0.8 \mathrm{~ms}, 1.6$ $\mathrm{ms}, 3.2 \mathrm{~ms}, 6.4 \mathrm{~ms}$ and 12.8 ms , selectable in groups of four |
| - at "0" to "1", min. | 0.2 ms | 0.2 ms | 0.2 ms | 0.2 ms | 0.2 ms | 0.2 ms |
| - at "0" to "1", max. | 12.8 ms | 12.8 ms | 12.8 ms | 12.8 ms | 12.8 ms | 12.8 ms |
| for interrupt inputs |  |  |  |  |  |  |
| - parameterizable | Yes | Yes | Yes | Yes | Yes | Yes |
| for technological functions |  |  |  |  |  |  |
| - parameterizable | Single phase: 3 <br> @ 100 kHz \& 3 <br> @ 30 kHz, <br> differential: 3 @ <br> 80 kHz \& 3 @ <br> 30 kHz | Single phase: 3 <br> @ 100 kHz \& 3 <br> @ 30 kHz, <br> differential: 3 @ <br> 80 kHz \& 3 @ <br> 30 kHz | Single phase: 3 <br> @ 100 kHz \& 3 <br> @ 30 kHz , <br> differential: 3 @ <br> 80 kHz \& 3 @ <br> 30 kHz | Single phase: 3 <br> @ 100 kHz \& 3 <br> @ 30 kHz, <br> differential: 3 @ <br> 80 kHz \& 3 @ <br> 30 kHz | Single phase: 3 @ 100 kHz \& 3 @ 30 kHz , differential: 3 @ 80 kHz \& 3 @ 30 kHz | Single phase: 3 @ $100 \mathrm{kHz} \& 3$ @ 30 kHz , differential: 3 @ 80 kHz \& 3 @ 30 kHz |
| Cable length |  |  |  |  |  |  |
| - shielded, max. | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions | $500 \mathrm{~m} ; 50 \mathrm{~m}$ for technological functions |
| - unshielded, max. | 300 m ; for technological functions: No | 300 m ; for technological functions: No | 300 m ; for technological functions: No | 300 m ; for technological functions: No | 300 m ; for technological functions: No | 300 m ; for technological functions: No |
| Digital outputs |  |  |  |  |  |  |
| Number of digital outputs | 6 | 6; Relays | 10 | 10; Relays | 10 | 10; Relays |
| - of which high-speed outputs | 4; 100 kHz Pulse Train Output |  | 4; 100 kHz Pulse Train Output |  | 4; 100 kHz Pulse Train Output |  |
| Limitation of inductive shutdown voltage to | L+ (-48 V) |  | L+(-48 V) |  | L+(-48 V) |  |
| Switching capacity of the outputs |  |  |  |  |  |  |
| - with resistive load, max. | 0.5 A | 2 A | 0.5A | 2 A | 0.5 A | 2 A |
| - on lamp load, max. | 5 W | 30 W with DC, <br> 200 W with AC | 5 W | 30 W with DC, 200 W with AC | 5 W | 30 W with DC, 200 W with AC |
| Output voltage |  |  |  |  |  |  |
| - for signal "0", max. | 0.1 V ; with 10 kOhm load |  | 0.1 V ; with 10 kOhm load |  | 0.1 V ; with 10 kOhm load |  |
| - for signal "1", min. | 20 V |  | 20 V |  | 20 V |  |
| Output current |  |  |  |  |  |  |
| - for signal "1" rated value | 0.5 A |  | 0.5A |  | 0.5A |  |
| - for signal " 0 " residual current, max. | 0.1 mA |  | 0.1 mA |  | 0.1 mA |  |
| Output delay with resistive load |  |  |  |  |  |  |
| - "0" to "1", max. | 1 нs | 10 ms ; max. | 1 ¢s | 10 ms ; max. | 1 нs | 10 ms ; max. |
| - "1" to "0", max. | $5 \mu \mathrm{~s}$ | 10 ms ; max. | $5 \mu \mathrm{~s}$ | 10 ms ; max. | $5 \mu \mathrm{~s}$ | 10 ms ; max. |
| Switching frequency <br> - of the pulse outputs, with resistive load max | 100 kHz |  | 100 kHz |  | 100 kHz |  |


| Article number | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { 0XB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, <br> 14DI/10DO/2AI | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, DC/DC/DC, <br> 14DI/10DO/2AI/2AO | CPU 1215 FC, <br> DC/DC/RLY,14DI/10DO/2AI/2AO |
| Relay outputs |  |  |  |  |  |  |
| - Number of relay outputs | 0 | 6 | 0 | 10 | 0 | 10 |
| - Number of operating cycles, max. |  | mechanically 10 million, at rated load voltage 100 000 |  | mechanically 10 million, at rated load voltage 100 000 |  | mechanically 10 million, at rated load voltage 100000 |
| Cable length |  |  |  |  |  |  |
| - shielded, max. | 500 m | 500 m | 500 m | 500 m | 500 m | 500 m |
| - unshielded, max. | 150 m | 150 m | 150 m | 150 m | 150 m | 150 m |
| Analog inputs |  |  |  |  |  |  |
| Number of analog inputs | 2 | 2 | 2 | 2 | 2 | 2 |
| Input ranges |  |  |  |  |  |  |
| - Voltage | Yes | Yes | Yes | Yes | Yes | Yes |
| Input ranges (rated values), voltages |  |  |  |  |  |  |
| - 0 to +10 V | Yes | Yes | Yes | Yes | Yes | Yes |
| $\begin{aligned} & \text { - Input resistance ( } 0 \text { to } \\ & 10 \mathrm{~V} \text { ) } \end{aligned}$ | $\geq 100 \mathrm{k}$ ohms | $\geq 100 \mathrm{k}$ ohms | $\geq 100 \mathrm{k}$ ohms | $\geq 100 \mathrm{k}$ ohms | $\geq 100 \mathrm{k}$ ohms | $\geq 100 \mathrm{k}$ ohms |
| Cable length |  |  |  |  |  |  |
| - shielded, max. | 100 m ; twisted and shielded | 100 m ; twisted and shielded | 100 m ; twisted and shielded | 100 m ; twisted and shielded | 100 m ; twisted and shielded | 100 m ; twisted and shielded |
| Analog outputs |  |  |  |  |  |  |
| Number of analog outputs | 0 | 0 | 0 | 0 | 2 | 2 |
| Output ranges, current |  |  |  |  |  |  |
| - 0 to 20 mA |  |  |  |  | Yes | Yes |
| Analog value generation for the inputs |  |  |  |  |  |  |
| Integration and conversion time/resolution per channel |  |  |  |  |  |  |
| - Resolution with overrange (bit including sign), max. | 10 bit | 10 bit | 10 bit | 10 bit | 10 bit | 10 bit |
| - Integration time, parameterizable | Yes | Yes | Yes | Yes | Yes | Yes |
| - Conversion time (per channel) | 625 s | 625 нs | 625 нs | 625 нs | $625 \mu$ s | 625 s |
| Analog value generation for the outputs |  |  |  |  |  |  |
| Integration and conversion time/resolution per channel |  |  |  |  |  |  |
| - Resolution with overrange (bit including sign), max. |  |  |  |  | 10 bit | 10 bit |
| Encoder |  |  |  |  |  |  |
| Connectable encoders <br> - 2-wire sensor | Yes | Yes | Yes | Yes | Yes | Yes |
| 1. Interface |  |  |  |  |  |  |
| Interface type | PROFINET | PROFINET | PROFINET | PROFINET | PROFINET | PROFINET |
| Isolated | Yes | Yes | Yes | Yes | Yes | Yes |
| automatic detection of transmission rate | Yes | Yes | Yes | Yes | Yes | Yes |
| Autonegotiation | Yes | Yes | Yes | Yes | Yes | Yes |
| Autocrossing | Yes | Yes | Yes | Yes | Yes | Yes |
| Interface types |  |  |  |  |  |  |
| - RJ 45 (Ethernet) | Yes |  | Yes | Yes | Yes | Yes |
| - Number of ports | 1 | 1 | 1 | 1 | 2 | 2 |
| - integrated switch | No | No | No | No | Yes | Yes |
| Protocols |  |  |  |  |  |  |
| - PROFINET Io | Yes | Yes | Yes | Yes | Yes | Yes |
| Controller |  |  |  |  |  |  |
| - PROFINET IO Device | Yes | Yes | Yes | Yes | Yes | Yes |
| - SIMATIC communication | Yes | Yes | Yes | Yes | Yes | Yes |
| - Open IE communication | Yes; Optionally also encrypted | Yes; Optionally also encrypted | Yes; Optionally also encrypted | Yes; Optionally also encrypted | Yes; Optionally also encrypted | Yes; Optionally also encrypted |
| - Web server | Yes | Yes | Yes | Yes | Yes | Yes |
| - Media redundancy | No | No | No | No | Yes; as MRP client | Yes |
| PROFINET IO Controller |  |  |  |  |  |  |
| - Transmission rate, max. | $100 \mathrm{Mbit} / \mathrm{s}$ | $100 \mathrm{Mbit} / \mathrm{s}$ | $100 \mathrm{Mbit} / \mathrm{s}$ | $100 \mathrm{Mbit} / \mathrm{s}$ | $100 \mathrm{Mbit} / \mathrm{s}$ | $100 \mathrm{Mbit} / \mathrm{s}$ |
| Services |  |  |  |  |  |  |
| - PG/OP <br> communication | Yes; encryption with TLS V1.3 pre-selected | Yes; encryption with TLS V1.3 pre-selected | Yes; encryption with TLS V1.3 pre-selected | Yes; encryption with TLS V1.3 pre-selected | Yes; encryption with TLS V1.3 preselected | Yes; encryption with TLS V1.3 pre-selected |
| - Isochronous mode | No | No | No | No | No | No |
| - IRT | No | No | No | No | No | No |
| - PROFlenergy | No | No | No | No | No | No |
| - Prioritized startup | Yes | Yes | Yes | Yes | Yes | Yes |
| - Number of IO devices | 16 | 16 | 16 | 16 | 16 | 16 |



| Article number | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { 0XB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, 14DI/10DO/2A | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, <br> DC/DC/DC, <br> 14DI/10DO/2AI/2AO | CPU 1215 FC, DC/DC/RLY,14DI/10DO/2AI/2AO |
| - OPC UA Server | Yes; data access (read, write, subscribe), method call, runtime license required | Yes; data access (read, write, subscribe), method call, runtime license required | Yes; data access (read, write, subscribe), method call, runtime license required | Yes; data access (read, write, subscribe), method call, runtime license required | Yes; data access (read, write, subscribe), method call, runtime license required | Yes; data access (read, write, subscribe), method call, runtime license required |
| - Application authentication | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 | Available <br> security policies: <br> None, <br> Basic128Rsa15, <br> Basic256Rsa15, <br> Basic256Sha256 | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 | Available security policies: <br> None, Basic128Rsa15, <br> Basic256Rsa15, <br> Basic256Sha256 |
| - User authentication | "anonymous" or by user name \& password | "anonymous" or by user name \& password | "anonymous" or by user name \& password | "anonymous" or by user name \& password | "anonymous" or by user name \& password | "anonymous" or by user name \& password |
| - Number of sessions, max. | 10 | 10 | 10 | 10 | 10 | 10 |
| - Number of subscriptions per session, max. | 5 | 5 | 5 | 5 | 5 | 5 |
| - Sampling interval, min. | 100 ms | 100 ms | 100 ms | 100 ms | 100 ms | 100 ms |
| - Publishing interval, min. | 200 ms | 200 ms | 200 ms | 200 ms | 200 ms | 200 ms |
| - Number of server methods, max. | 20 | 20 | 20 | 20 | 20 | 20 |
| - number of monitored items, recommended max. | 1000 | 1000 | 1000 | 1000 | 1000 | 1000 |
| - Number of server interfaces, max. | 2 | 2 | 2 | 2 | 2 | 2 |
| - Number of nodes for user-defined server interfaces, max. | 2000 | 2000 | 2000 | 2000 | 2000 | 2000 |
| Further protocols |  |  | Yes | Yes | Yes | Yes |
| communication functions / header |  |  |  |  |  |  |
| S7 communication |  |  |  |  |  |  |
| - supported | Yes | Yes | Yes | Yes | Yes | Yes |
| - as server | Yes | Yes | Yes | Yes | Yes | Yes |
| - as client | Yes | Yes | Yes | Yes | Yes | Yes |
| - User data per job, max. | See online help (S7 communication, user data size) | See online help (S7 communication, user data size) | See online help (S7 communication, user data size) | See online help (S7 communication, user data size) | See online help (S7 communication, user data size) | See online help (S7 communication, user data size) |
| Number of connections |  |  |  |  |  |  |
| - overall | PG Connections: 4 reserved / 4 max; HMI Connections: 12 reserved / 18 max; S7 Connections: 8 reserved / 14 max; Open User Connections: 8 reserved / 14 max; Web Connections: 2 reserved / 30 max; OPC UA Connections: 0 reserved / 10 max; Total Connections: 34 reserved / 64 max | PG Connections: 4 reserved / 4 max; HMI <br> Connections: 12 <br> reserved / 18 <br> max; S7 <br> Connections: 8 <br> reserved / 14 <br> max; Open User <br> Connections: 8 <br> reserved / 14 <br> max; Web <br> Connections: 2 <br> reserved / 30 <br> max; OPC UA <br> Connections: 0 <br> reserved / 10 <br> max; Total <br> Connections: 34 <br> reserved / 64 <br> max | PG Connections: 4 reserved / 4 max; HMI <br> Connections: 12 <br> reserved / 18 max; S7 <br> Connections: 8 <br> reserved / 14 <br> max; Open User <br> Connections: 8 <br> reserved / 14 <br> max; Web <br> Connections: 2 <br> reserved / 30 <br> max; OPC UA <br> Connections: 0 <br> reserved / 10 <br> max; Total <br> Connections: 34 <br> reserved / 64 <br> max | PG Connections: 4 reserved / 4 max; HMI Connections: 12 reserved / 18 max; $\mathrm{S7}$ Connections: 8 reserved / 14 max; Open User Connections: 8 reserved / 14 max; Web Connections: 2 reserved / 30 max; OPC UA Connections: 0 reserved / 10 max; Total Connections: 34 reserved / 64 max | PG Connections: 4 reserved / 4 max; HMI Connections: 12 reserved / 18 max; S7 Connections: 8 reserved / 14 max; Open User Connections: 8 reserved / 14 max; Web Connections: 2 reserved / 30 max; OPC UA Connections: 0 reserved / 10 max; Total Connections: 34 reserved / 64 max | PG Connections: 4 reserved / 4 max; HMI Connections: 12 reserved / 18 max; S7 Connections: 8 reserved / 14 max; Open User Connections: 8 reserved / 14 max; Web Connections: 2 reserved / 30 max; OPC UA Connections: 0 reserved / 10 max; Total Connections: 34 reserved / 64 max |
| Test commissioning functions |  |  |  |  |  |  |
| Status/control |  |  |  |  |  |  |
| - Status/control variable | Yes | Yes | Yes | Yes | Yes | Yes |
| - Variables | inputs/outputs, bit memories, DBs, peripheral I/Os (without failsafe), times, counters | inputs/outputs, bit memories, DBs, peripheral I/Os (without failsafe), times, counters | inputs/outputs, bit memories, DBs, peripheral I/Os (without failsafe), times, counters | inputs/outputs, bit memories, DBs, peripheral I/Os (without failsafe), times, counters | inputs/outputs, bit memories, DBs, peripheral I/Os (without fail-safe), times, counters | inputs/outputs, bit memories, DBs, peripheral I/Os (without fail-safe), times, counters |
| Forcing |  |  |  |  |  |  |
| - Forcing | Yes; peripheral inputs/outputs (without failsafe) | Yes; peripheral inputs/outputs (without failsafe) | Yes; peripheral inputs/outputs (without failsafe) | Yes; peripheral inputs/outputs (without failsafe) | Yes; peripheral inputs/outputs (without fail-safe) | Yes; peripheral inputs/outputs (without fail-safe) |
| Diagnostic buffer |  |  |  |  |  | Yes |
| Traces |  |  |  |  |  |  |
| - Number of configurable Traces | 2 | 2 | 2 | 2 | 2 | 2 |
| - Memory size per trace, max. | 512 kbyte | 512 kbyte | 512 kbyte | 512 kbyte | 512 kbyte | 512 kbyte |


| Article number | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { 0XB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, 14DI/10DO/2AI | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, DC/DC/DC, 14DI/10DO/2AI/2AO | CPU 1215 FC, DC/DC/RLY,14DI/10DO/2AI/2AO |
| Interrupts/diagnostics/status information |  |  |  |  |  |  |
| Diagnostics indication LED |  |  |  |  |  |  |
| - RUN/Stop led | Yes | Yes | Yes | Yes | Yes | Yes |
| - ERror led | Yes | Yes | Yes | Yes | Yes | Yes |
| - maint led | Yes | Yes | Yes | Yes | Yes | Yes |
| Integrated Functions |  |  |  |  |  |  |
| Frequency measurement | Yes | Yes | Yes | Yes | Yes | Yes |
| controlled positioning | Yes | Yes | Yes | Yes | Yes | Yes |
| Number of position-controlled positioning axes, max. | 8 | 8 | 8 | 8 | 8 | 8 |
| Number of positioning axes via pulse-direction interface | Up to 4 with SB 1222 | Up to 4 with SB 1222 | 4; With integrated outputs | Up to 4 with SB 1222 | 4; With integrated outputs | Up to 4 with SB 1222 |
| PID controller | Yes | Yes | Yes | Yes | Yes | Yes |
| Number of alarm inputs | 4 | 4 | 4 | 4 | 4 | 4 |
| Number of pulse outputs | 4 |  | 4 |  | 4 |  |
| Limit frequency (pulse) | 100 kHz |  | 100 kHz |  | 100 kHz |  |
| Potential separation |  |  |  |  |  |  |
| Potential separation digital inputs |  |  |  |  |  |  |
| - Potential separation digital inputs | No | 500 V AC for 1 minute | No | 500 V AC for 1 minute | No | 500 VAC for 1 minute |
| - between the channels, in groups of | 1 | 1 | 1 | 1 | 1 | 1 |
| Potential separation digital outputs |  |  |  |  |  |  |
| - Potential separation digital outputs | Yes | Relays | Yes | Relays | Yes | Relays |
| - between the channels | No | No | No | No | No | No |
| - between the channels, in groups of | 1 | 2 | 1 | 2 | 1 | 2 |
| EMC |  |  |  |  |  |  |
| Interference immunity against discharge of static electricity |  |  |  |  |  |  |
| - Interference immunity against discharge of static electricity acc. to IEC 61000-4-2 | Yes | Yes | Yes | Yes | Yes | Yes |
| - Test voltage at air discharge | 8 kV | 8 kV | 8 kV | 8 kV | 8 kV | 8 kV |
| — Test voltage at contact discharge | 6 kV | 6 kV | 6 kV | 6 kV | 6 kV | 6 kV |
| Interference immunity to cable-borne interference |  |  |  |  |  |  |
| - Interference immunity on supply lines acc. to IEC 61000-4-4 | Yes | Yes | Yes | Yes | Yes | Yes |
| - Interference immunity on signal cables acc. to IEC 61000-4-4 | Yes | Yes | Yes | Yes | Yes | Yes |
| Interference immunity against voltage surge |  |  |  |  |  |  |
| - Interference immunity on supply lines acc. to IEC 61000-4-5 | Yes | Yes | Yes | Yes | Yes | Yes |
| Interference immunity against conducted variable disturbance induced by high-frequency fields |  |  |  |  |  |  |
| - Interference immunity against high-frequency radiation acc. to IEC 61000-4-6 | Yes | Yes | Yes | Yes | Yes | Yes |
| Emission of radio interference acc. to EN 55 011 |  |  |  |  |  |  |
| - Limit class A , for use in industrial areas | Yes; Group 1 | Yes; Group 1 | Yes; Group 1 | Yes; Group 1 | Yes; Group 1 | Yes; Group 1 |
| - Limit class B, for use in residential areas | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 | Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011 |
| Degree and class of protection |  |  |  |  |  |  |
| IP degree of protection | IP20 | IP20 | IP20 | IP20 | IP20 | IP20 |


| Article number | $\begin{aligned} & \hline \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \hline \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { OXB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, 14DI/10DO/2AI | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, DC/DC/DC, 14DI/10DO/2AI/2AO | CPU 1215 FC, DC/DC/RLY,14DI/10DO/2AI/2AO |
| CE mark | Yes | Yes | Yes | Yes | Yes | Yes |
| UL approval | Yes | Yes | Yes | Yes | Yes | Yes |
| cULus | Yes | Yes | Yes | Yes | Yes | Yes |
| FM approval | Yes | Yes | Yes | Yes | Yes | Yes |
| RCM (formerly C-TICK) | Yes | Yes | Yes | Yes | Yes | Yes |
| KC approval | Yes | Yes | Yes | Yes | Yes | Yes |
| Marine approval | Yes | Yes | Yes | Yes | Yes | Yes |
| Highest safety class achievable in safety mode |  |  |  |  |  |  |
| - Performance level according to ISO 13849-1 | PLe | PLe | PLe | PLe | PLe | PLe |
| - SIL acc. to IEC 61508 | SIL 3 | SIL 3 | SIL 3 | SIL 3 | SIL 3 | SIL 3 |

Ambient conditions
Free fall

| - Fall height, max. | 0.3 m ; five times, in product package | 0.3 m ; five times, in product package | 0.3 m ; five times, in product package | 0.3 m ; five times, in product package | 0.3 m ; five times, in product package | 0.3 m ; five times, in product package |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Ambient temperature during operation |  |  |  |  |  |  |
| - min. | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0{ }^{\circ} \mathrm{C}$ |
| - max. | $55^{\circ} \mathrm{C}$; Number of <br> simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or 50 ${ }^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or 45 ${ }^{\circ} \mathrm{C}$ vertical | $55^{\circ} \mathrm{C}$; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or 50 ${ }^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or 45 ${ }^{\circ} \mathrm{C}$ vertical | $55^{\circ} \mathrm{C}$; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or 50 ${ }^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or 45 ${ }^{\circ} \mathrm{C}$ vertical | $55^{\circ} \mathrm{C}$; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or 50 ${ }^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or 45 ${ }^{\circ} \mathrm{C}$ vertical | $55^{\circ} \mathrm{C}$; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or $50^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or $45^{\circ} \mathrm{C}$ vertical | $55^{\circ} \mathrm{C}$; Number of simultaneously activated inputs or outputs 4 or 3 (no adjacent points) at $60^{\circ} \mathrm{C}$ horizontal or 50 ${ }^{\circ} \mathrm{C}$ vertical, 8 or 6 at $55^{\circ} \mathrm{C}$ horizontal or $45^{\circ} \mathrm{C}$ vertical |
| - horizontal installation, | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ |
| - horizontal installation, max. | $55^{\circ} \mathrm{C}$ | $55^{\circ} \mathrm{C}$ | $55^{\circ} \mathrm{C}$ | $55^{\circ} \mathrm{C}$ | $55^{\circ} \mathrm{C}$ | $55^{\circ} \mathrm{C}$ |
| - vertical installation, min. | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ |
| - vertical installation, max. | $45^{\circ} \mathrm{C}$ | $45^{\circ} \mathrm{C}$ | $45^{\circ} \mathrm{C}$ | $45^{\circ} \mathrm{C}$ | $45^{\circ} \mathrm{C}$ | $45^{\circ} \mathrm{C}$ |


| Ambient temperature during storage/transportation |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| - min. | $-40^{\circ} \mathrm{C}$ | $-40^{\circ} \mathrm{C}$ | $-40^{\circ} \mathrm{C}$ | $-40^{\circ} \mathrm{C}$ | $-40^{\circ} \mathrm{C}$ | $-40^{\circ} \mathrm{C}$ |
| - max. | $70^{\circ} \mathrm{C}$ | $70^{\circ} \mathrm{C}$ | $70^{\circ} \mathrm{C}$ | $70^{\circ} \mathrm{C}$ | $70^{\circ} \mathrm{C}$ | $70^{\circ} \mathrm{C}$ |
| Air pressure acc. to IEC 60068-2-13 |  |  |  |  |  |  |
| - Operation, min. | 795 hPa | 795 hPa | 795 hPa | 795 hPa | 795 hPa | 795 hPa |
| - Operation, max. | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa |
| - Storage/transport, min. | 660 hPa | 660 hPa | 660 hPa | 660 hPa | 660 hPa | 660 hPa |
| - Storage/transport, max. | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa | 1080 hPa |
| Altitude during operation relating to sea level |  |  |  |  |  |  |
| - Installation altitude, min. | -1000 m | -1 000 m | -1000 m | -1000 m | -1000 m | -1 000 m |
| - Installation altitude, max. | $5000 \mathrm{~m} ;$ Restrictions for installation altitudes > 2000 m , see manual | 5000 m ; Restrictions for installation altitudes > 2000 m , see manual | $5000 \mathrm{~m} ;$ Restrictions for installation altitudes > 2000 m , see manual | $5000 \mathrm{~m} ;$ Restrictions for installation altitudes > 2000 m , see manual | 5000 m ; Restrictions for installation altitudes $>2000 \mathrm{~m}$, see manual | 5000 m ; Restrictions for installation altitudes $>2000 \mathrm{~m}$, see manual |
| Relative humidity |  |  |  |  |  |  |
| - Operation, max. | $95 \%$; no condensation | $\begin{aligned} & 95 \% ; \text { no } \\ & \text { condensation } \end{aligned}$ | $\begin{aligned} & 95 \% ; \text { no } \\ & \text { condensation } \end{aligned}$ | $\begin{aligned} & 95 \% \text {; no } \\ & \text { condensation } \end{aligned}$ | $95 \% \text {; no }$ condensation | $95 \%$; no condensation |
| Vibrations |  |  |  |  |  |  |
| - Vibration resistance during operation acc. to IEC 60068-2-6 | $2 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ wall mounting, 1 g ( $\mathrm{m} / \mathrm{s}^{2}$ ) DIN rail | $\left.2 \mathrm{~g} \mathrm{(m/s}^{2}\right)$ wall mounting, 1 g $\left(\mathrm{m} / \mathrm{s}^{2}\right)$ DIN rail | $2 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ wall mounting, 1 g ( $\mathrm{m} / \mathrm{s}^{2}$ ) DIN rail | $2 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ wall mounting, 1 g ( $\mathrm{m} / \mathrm{s}^{2}$ ) DIN rail | $2 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ wall mounting, $1 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ DIN rail | $2 \mathrm{~g}\left(\mathrm{~m} / \mathrm{s}^{2}\right)$ wall mounting, 1 g ( $\mathrm{m} / \mathrm{s}^{2}$ ) DIN rail |
| - Operation, tested according to IEC 60068-26 | Yes | Yes | Yes | Yes | Yes | Yes |
| Shock testing |  |  |  |  |  |  |
| - tested according to IEC 60068-2-27 | Yes; IEC 68, Part 2-27 halfsine: strength of the shock 15 g (peak value), duration 11 ms | Yes; IEC 68, Part 2-27 halfsine: strength of the shock 15 g (peak value), duration 11 ms | Yes; IEC 68, Part 2-27 halfsine: strength of the shock 15 g (peak value), duration 11 ms | Yes; IEC 68, Part 2-27 halfsine: strength of the shock 15 g (peak value), duration 11 ms | Yes; IEC 68, Part 227 half-sine: strength of the shock 15 g (peak value), duration 11 ms | Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms |
| Pollutant concentrations |  |  |  |  |  |  |
| - SO2 at RH < 60\% <br> without condensation | S02: < 0.5 ppm ; <br> H2S: < 0.1 ppm ; RH < 60\% condensationfree | $\begin{aligned} & \mathrm{SO2:}:<0.5 \mathrm{ppm} ; \\ & \mathrm{H} 2 \mathrm{~S}:<0.1 \mathrm{ppm} \\ & \mathrm{RH}<60 \% \\ & \text { condensation- } \\ & \text { free } \end{aligned}$ | $\begin{aligned} & \mathrm{S} 02:<0.5 \mathrm{ppm} ; \\ & \mathrm{H} 2 \mathrm{~S}:<0.1 \mathrm{ppm} ; \\ & \mathrm{RH}<60 \% \\ & \text { condensation- } \\ & \text { free } \end{aligned}$ | $\begin{aligned} & \mathrm{SO2:}:<0.5 \mathrm{ppm} ; \\ & \mathrm{H} 2 \mathrm{~S}:<0.1 \mathrm{ppm} \\ & \mathrm{RH}<60 \% \\ & \text { condensation- } \\ & \text { free } \end{aligned}$ | $\begin{aligned} & \text { S02: < } 0.5 \mathrm{ppm} ; \\ & \mathrm{H} 2 \mathrm{~S}:<0.1 \mathrm{ppm} ; \mathrm{RH} \\ & <60 \% \\ & \text { condensation-free } \end{aligned}$ | S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60\% condensationfree |

configuration / header
configuration / programming
/ header
Programming language

| — LAD | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| — FBD | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe | Yes; incl. failsafe |
| —SCL | Yes | Yes | Yes | Yes | Yes | Yes |

Know-how protection

| Article number | $\begin{aligned} & \text { 6ES7212- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7212- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7214- } \\ & \text { 1AF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7214- } \\ & \text { 1HF40-0XB0 } \end{aligned}$ | $\begin{aligned} & \text { 6ES7215-1AF40- } \\ & \text { 0XB0 } \end{aligned}$ | 6ES7215-1HF40-0XB0 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CPU 1212FC ,DC/DC/DC, 8DI/6DO/2AI | CPU 1212FC, DC/DC/Relay, 8DI/6DO/2AI | CPU 1214 FC, DC/DC/DC, <br> 14DI/10DO/2AI | CPU 1214 FC, DC/DC/Relay, 14DI/10DO/2AI | CPU 1215 FC, DC/DC/DC, 14DI/10DO/2AI/2AO | CPU 1215 FC, DC/DC/RLY,14DI/10DO/2AI/2AO |
| - User program protection/password protection | Yes | Yes | Yes | Yes | Yes | Yes |
| - Copy protection | Yes | Yes | Yes | Yes | Yes | Yes |
| - Block protection | Yes | Yes | Yes | Yes | Yes | Yes |
| Access protection <br> - protection of confidential configuration data | Yes |  | Yes | Yes | Yes | Yes |
| - Protection level: Write protection | Yes | Yes | Yes | Yes | Yes | Yes |
| - Protection level: | Yes | Yes | Yes | Yes | Yes | Yes |
| Read/write protection <br> - Protection level: <br> Complete protection | Yes | Yes | Yes | Yes | Yes | Yes |
| programming / cycle time monitoring / header <br> - adjustable | Yes | Yes | Yes | Yes | Yes | Yes |
| Dimensions |  |  |  |  |  |  |
| Width | 90 mm | 90 mm | 110 mm | 110 mm | 130 mm | 130 mm |
| Height | 100 mm | 100 mm | 100 mm | 100 mm | 100 mm | 100 mm |
| Depth | 75 mm | 75 mm | 75 mm | 75 mm | 75 mm | 75 mm |
| Weights |  |  |  |  |  |  |
| Weight, approx. | 370 g | 385 g | 415 g | 435 g | 500 g | 585 g |

