

Allen Bradley MicroLogix

HMI Factory Setting:

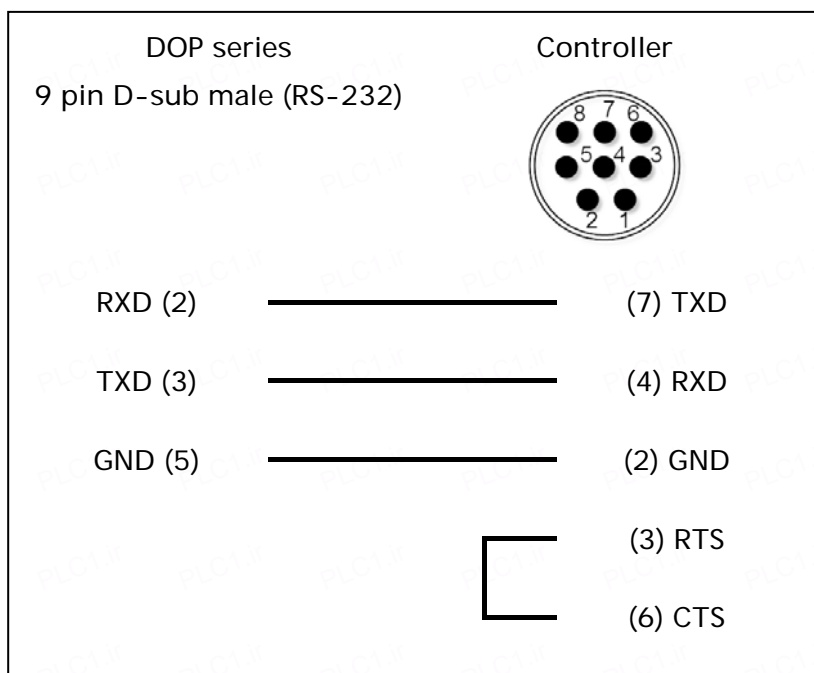
Baud Rate: 192.168.0.1

Controller Station Number: 1

Control Area / Status Area: B3:0/B3:10

Connection

a. RS-232 (DOP-A/AE/AS, DOP-B Series)



Definition of PLC Read/Write Address

a. Registers

Type	Format	Read/Write Range	Data Length	Note
	Word No. (n) File No. (f)			
Output file	O:n	O:0 – O:255 (f = 0)	Word	
	O:s.n	O:0.0 – O:8.255 (f = 0)		
Input file	I:n	I:0 – I:255 (f = 1)	Word	
	I:s.n	I:0.0 – I:7.255 (f = 1)		
Status file	S2:n	S2:0 – S2:255 (f = 2)	Word	
Bit file	B:n	B:0 – B:255 (f = 3)	Word	
	Bf:n	B3:0 – B3:255, B9:0 – B255:255		
Timer flag	T:n	T:0 – T:255 (f = 4)	Word	
	Tf:n	T4:0 – T4:255, T9:0 – T255:255		
Timer Preset Value	T:n.PRE	T:0.PRE – T:255.PRE (f = 4)	Word	
	Tf:n.PRE	T4:0.PRE – T4:255.PRE, T9:0.PRE – T255:255.PRE		
Timer Accumulator Value	T:n.ACC	T:0.ACC – T:255.ACC, (f = 4)	Word	
	Tf:n.ACC	T4:0.ACC – T4:255.ACC, T9:0.ACC – T255:255.ACC		
Counter flag	C:n	C:0 – C:255, (f = 5)	Word	
	Cf:n	C5:0 – C5:255, C9:0 – C255:255		
Counter Preset Value	C:n.PRE	C:0.PRE – C:255.PRE, (f = 5)	Word	
	Cf:n.PRE	C5:0.PRE – C5:255.PRE, C9:0.PRE – C255:255.PRE		
Counter Accumulator Value	C:n.ACC	C:0.ACC – C:255.ACC, (f = 5)	Word	
	Cf:n.ACC	C5:0.ACC – C5:255.ACC, C9:0.ACC – C255:255.ACC		
Control file	R:n	R:0 – R:255, (f = 6)	Word	
	Rf:n	R6:0 – R6:255, R9:0 – R255:255		
Control Size of Bit Array	R:n.LEN	R:0.LEN – R:255.LEN, (f = 6)	Word	
	Rf:n.LEN	R6:0.LEN – R6:255.LEN, R9:0.LEN – R255:255.LEN		
Control Reserved file	R:n.POS	R:0.POS – R:255.POS, (f = 6)	Word	

Type	Format	Read/Write Range	Data Length	Note
	Word No. (n) File No. (f)			
	Rf:n.POS	R6:0.POS – R6:255.POS, R9:0.POS – R255:255.POS		
Integer file	N:n	N:0 – N:255, (f = 7)	Word	
	Nf:n	N7:0 – N7:255, N9:0 – N255:255		
Floating Point file	F:n	F:0 – F:255, (f = 8)	Double Word	2
	Ff:n	F8:0 – F8:255, F9:0 – F255:255		
String File	STf:n	ST9:0 – ST255:255	41 Words	
Long Word File	Lf:n	L9:0 – L255:255	Double Word	

b. Contacts

Type	Format	Read/Write Range	Note
	Word No. (n) Bit No. (b) File No. (f)		
Output	O:n/b	O:0/0 – O:255/15 (f = 0)	
	O:s.n/b	O:0.0/0 – O:8.255/15 (f = 0)	
Input	I:n/b	I:0/0 – I:255/15 (f = 1)	
	I:s.n/b	I:0.0/0 – I:8.255/15 (f = 1)	
Status	S2:n/b	S2:0/0 – S2:255/15 (f = 2)	
Bit	B:n/b	B:0/0 – B:255/15, (f = 3)	
	Bf:n/b	B3:0/0 – B3:255/15, B9:0/0 – B255:255/15	
Timer	T:n/b	T:0/0 – T:255/15, (f = 4)	
	Tf:n/b	T4:0/0 – T4:255/15, T9:0/0 – T255:255/15	
	T:n/EN	T:0/EN – T:255/EN, (b = 15) (f = 4)	
	Tf:n/EN	T4:0/EN – T4:255/EN, (b = 15), T9:0/EN – T255:255/EN (b = 15)	
	T:n/TT	T:0/TT – T:255/TT, (b = 14) (f = 4)	
	Tf:n/TT	T4:0/TT – T4:255/TT, (b = 14) T9:0/TT – T255:255/TT (b = 14)	
	T:n/DN	T:0/TT – T:255/TT, (b = 13), (f = 4)	
	Tf:n/DN	T4:0/TT – T4:255/TT, (b = 13) T9:0/TT – T255:255/TT (b = 13)	
Timer Preset Value	T:n.PRE/b	T:0.PRE/0 – T:255.PRE/15, (f = 4)	
	Tf:n.PRE/b	T4:0.PRE/0 – T4:255.PRE/15, T9:0.PRE/0 – T255:255.PRE/15	
Timer Accumulator Value	T:n.ACC/b	T:0.ACC/0 – T:255.ACC/15, (f = 4)	

	Tf:n.ACC/b	T4:0.ACC/0 – T4:255.ACC/15, T9:0.ACC/0 – T255:255.ACC/15	
Counter	C:n/b	C:0/0 – C:255/15, (f = 5)	
	Cf:n/b	C5:0/0 – C5:255/15, C9:0/0 – C255:255/15	
	C:n/CU	C:0/CU – C:255/CU, (b = 15) (f = 5)	
	Cf:n/CU	C5:0/CU – C5:255/CU, (b = 15) C9:0/CU – C255:255/CU (b = 15)	
	C:n/CD	C:0/CD – C:255/CD, (b = 14) (f = 5)	
	Cf:n/CD	C5:0/CD – C5:255/CD, (b = 14) C9:0/CD – C255:255/CD (b = 14)	
	C:n/DN	C:0/DN – C:255/DN, (b = 13) (f = 5)	
	Cf:n/DN	C5:0/DN – C5:255/DN, (b = 13) C9:0/DN – C255:255/DN (b = 13)	
	C:n/OV	C:0/OV – C:255/OV, (b = 12) (f = 5)	
	Cf:n/OV	C5:0/OV – C5:255/OV, (b = 12) C9:0/OV – C255:255/OV (b = 12)	
	C:n/UN	C:0/UN – C:255/UN, (b = 11) (f = 5)	
	Cf:n/UN	C5:0/UN – C5:255/UN, (b = 11) C9:0/UN – C255:255/UN (b = 11)	
	C:n/UA	C:0/UA – C:255/UA, (b = 10) (f = 5)	
	Cf:n/UA	C5:0/UA – C5:255/UA, (b = 10) C9:0/UA – C255:255/UA (b = 10)	
Counter Preset Value	C:n.PRE/b	C:0.PRE/0 – C:255.PRE/15, (f = 5)	
	Cf:n.PRE/b	C5:0.PRE/0 – C5:255.PRE/15, C9:0.PRE/0 – C255:255.PRE/15	
Counter Accumulator Value	C:n.ACC/b	C:0.PRE/0 – C:255.PRE/15, (f = 5)	
	Cf:n.ACC/b	C5:0.PRE/0 – C5:255.PRE/15, C9:0.PRE/0 – C255:255.PRE/15	
Control	R:n/b	R:0/0 – R:255/15, (f = 6)	
	Rf:n/b	R6:0/0 – R6:255/15, R9:0/0 – R255:255/15	
	R:n/EN	R:0/EN – R:255/EN, (b = 15) (f = 6)	
	Rf:n/EN	R6:0/EN – R6:255/EN, (b = 15) R9:0/EN – R255:255/EN (b = 15)	
	R:n/EU	R:0/EU – R:255/EU, (b = 14) (f = 6)	
	Rf:n/EU	R6:0/EU – R6:255/EU, (b = 14) R9:0/EU – R255:255/EU (b = 14)	
	R:n/DN	R:0/DN – R:255/DN, (b = 13) (f = 6)	
	Rf:n/DN	R6:0/DN – R6:255/DN, (b = 13) R9:0/DN – R255:255/DN (b = 13)	
	R:n/EM	R:0/EM – R:255/EM, (b = 12) (f = 6)	

	Rf:n/EM	R6:0/EM – R6:255/EM, (b = 12) R9:0/EM – R255:255/EM (b = 12)	
	R:n/ER	R:0/ER – R:255/ER, (b = 11) (f = 6)	
	Rf:n/ER	R6:0/ER – R6:255/ER, (b = 11) R9:0/ER – R255:255/ER (b = 11)	
	R:n/UL	R:0/UL – R:255/UL, (b = 10) (f = 6)	
	Rf:n/UL	R6:0/UL – R6:255/UL, (b = 10) R9:0/UL – R255:255/UL (b = 10)	
	R:n/IN	R:0/IN – R:255/IN, (b = 9) (f = 6)	
	Rf:n/IN	R6:0/IN – R6:255/IN, (b = 9) R9:0/IN – R255:255/IN (b = 9)	
	R:n/FD	R:0/FD – R:255/FD, (b = 8) (f = 6)	
	Rf:n/FD	R6:0/FD – R6:255/FD, (b = 8) R9:0/FD – R255:255/FD (b = 8)	
Control Size of Bit Array	R:n.LEN/b	R:0.LEN/0 – R:255.LEN/15, (f = 6)	
	Rf:n.LEN/b	R6:0.LEN/0 – R6:255.LEN/15, R9:0.LEN/0 – R255:255.LEN/15	
Control Reserved	R:n.POS/b	R:0.POS/0 – R:255.POS/15, (f = 6)	
	Rf:n.POS/b	R6:0.POS/0 – R6:255.POS/15, R9:0.POS/0 – R255:255.POS/15	
Integer	N:n/b	N:0/0 – N:255/15, (f = 7)	
	Nf:n/b	N7:0/0 – N7:255/15, N9:0/0 – N255:255/15	
Long Word File	Lf:n/b	L9:0/0 – L255:255/31	

 **NOTE**

- 1) This protocol only supports DF1 protocol mode. Setting can be done on with panel located on the top of PLC. Setting parameter: [Advance Set](#) → [DCOMM Cfg](#) → [Enable](#) → [Comms config set to DF1 default](#)
- 2) This protocol supports BCC or CRC Error Check.